

Introducing Associate Editors: Adriano Valenzano



Adriano Valenzano is a native of Torino, Italy. In 1980 he graduated *summa cum laude* in Electronic Engineering at Politecnico di Torino, Italy.

From 1980 to 1983 Adriano Valenzano joined the Department of Computer and Systems Engineering at Politecnico di Torino, where he was engaged in researches on advanced parallel processors and special purpose processor architectures. In the same period he was also a consultant for a number of main Italian companies, including Alenia, Comau, Fiat and Prima Industrie, which were interested in industrial automation.

From 1983 to 1990 he was a tenured researcher with the Research Centre for Digital Signal Processing (CENS), Italy, an initiative established jointly by the Italian National Council of Research (CNR) and Politecnico di Torino. During his activity at CENS, Adriano Valenzano was deeply involved in the Italian National Computer Science Programme and focused, in particular, on the MODIAC (MODular Distributed system for Industrial Automation and process Control) and MUMICRO (MULTI-MICROprocessor Systems) projects. In that framework he led the research team that was responsible for designing and developing the MODIAC network, one of the first high-speed industrial LANs, which was able to connect multiprocessors nodes by means of the full ISO-OSI protocol stack hosted on a single

microprocessor board.

In 1991 Adriano Valenzano became Director of Research at CNR. Since then he has led several research teams, taking part into a number of national and international advanced research projects, supported by the Italian Ministry for Scientific Research, CNR and the European Union.

In 2002, following an in-depth reorganization of the CNR scientific network, Adriano Valenzano joined the Institute of Electronics and Computer and Telecommunications Engineering (IEIIT), Torino, where he is still the head of the Computer and Systems Engineering research group.

From 2000 to 2002 he was appointed as a national coordinator of the Italian ICT project "Techniques and Tools for the Development of Distributed Virtual Shops", carried out by the universities of Catania, Naples, Genoa and Torino. Since 2005 he is responsible for the national projects "Networks and Protocols for Automation and Process Control" and "Methods and Tools for the Design of Highly Complex Software-Intensive Systems" promoted by the ICT Department of CNR.

Adriano Valenzano has also been responsible for a number of research contracts co-funded by small and medium Italian enterprises and large international companies, including ST-Microelectronics, Electrolux-Zanussi (domestic appliances) and the Ferrero (sweets and chocolate) and SCM (woodworking machines) Groups. At present, he is also active in the "Dependability and Security by Enhanced Reconfigurability" (DESEREC) Integrated Project of the European Union 6th Framework Programme for research and technological development.

Adriano Valenzano has authored/co-authored more than 200 scientific papers published in refereed international journals and conference proceedings (including IEEE Trans. On Industrial Informatics, IEEE Trans. on Industrial Electronics, IEEE Trans. on Software Engineering, IEEE Trans. on Computers, IEEE/ACM Trans. on Networking and ACM Trans. on Software Engineering and Methodology) mainly in the areas of computers and networks, industrial communications and formal methods for the analysis of security properties of complex systems and networks. He has been awarded as the co-author of the best paper presented at WFCS 2004. He is also the co-author of the books "Advanced Microprocessor Architectures" and "MAP and TOP Communications: Standards and Applications" both published with Addison-Wesley Publishing Co.

During his scientific and professional activity, Adriano Valenzano has taken part in the program and organising committees of many international conferences in the area of industrial informatics, including several events in the IEEE IES ETFA, WFCS and IECON series. He also served as a general co-chairman for the 6th edition of WFCS, which was held in 2006.

For about 20 years Adriano Valenzano has been teaching courses on programming techniques, languages and operating systems to undergraduate students at the Engineering Faculty of Politecnico di Torino and has been an invited lecturer of a significant number of advanced seminars and courses attended by postgraduate students and professionals in the areas of industrial communications, fieldbuses, networks for automation and product data technology (PDT). Since 1990 Adriano Valenzano has been a member of the Board of Directors of the Italian National Association for Automation (ANIPLA) and since 2006 he has also been Vice President of the Piedmont Chapter of ANIPLA.

Adriano Valenzano is married to Giovanna and has one daughter; Marina, who is 18. In his spare time he loves skiing in the Western Alps, walking, reading books and listening to music.