



The 27th Annual Conference of the IEEE Industrial Electronics Society
Hyatt Regency Tech Center, Denver, Colorado, USA
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IECON'01

Sponsored by: IEEE Industrial Electronics Society
Tech. Co-Sponsored by: Society of Instrumentation and Control Engineers
University of Colorado at Denver
The University of Wyoming

Special Session on Low power Electronics

organized and co-chaired by
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Call For Papers

Topics of interest include, but are not limited to:

- High-performance low-power VLSI digital circuits
- Low voltage low-power logic families.
- Fast low-power arithmetic.
- Asynchronous and self-timed systems.
- FPGA-based low power design.
- Design methods and CAD tools for low power design.
- Other topics related to the conference scope and/or of interest to the prospective conference participants.

Submission of Papers: The working language of the conference is English. Submit a full paper using the following layout: Page 1: title, authors' names, mailing address, phone, fax, and email of each author. Identify the contact author, preferred topical area, subject area and keywords. Page 2: title, authors' affiliations, 100-200 words abstract. Pages 3-8: full paper limited to 6 pages, including figures and references. Submit full papers in postscript or pdf format by e-mail to vkantabu@computer.org.

Author's Schedule:

Deadline for Submission of Full Papers:

April 29, 2001

Notification of Acceptance:

June 10, 2001

Deadline for Submission of Final Manuscripts:

August 15, 2001

For general information please check the conference web page: <http://sant.bradley.edu/~iecon01/>

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